S	earch	Notes	

Application/Control No.	Applicant(s)/Patent under Reexamination
10/687,753	OKAMOTO ET AL.
Examiner	Art Unit
Abiy Getachew	2841

<del></del>	<del></del>		
	SEAR	CHED	
Class	Subclass	Date	Examiner
361	749	3/17/2007	A.G.
365	52	3/17/2007	A.G.
336	200	3/17/2007	A.G.
			,

INT	INTERFERENCE SEARCHED		
Class	Subclass	Date	Examiner
		-	
Interference See the at	e Searched- ttachment.	12/5/2007	AG

SEARCH NOT (INCLUDING SEARCH)		)
	DATE	EXMR
EAST searched (USPAT, USPGPUB, USOCR, EPO, JPO, DERWENT, IBMTDB) See Search History Printout	12/5/2007	AG
Consulted with A. Dexter Tugbang and Ishwar Patel	12/5/2007	A.G.
(electronic adj circuit and electric and foldable and rectangular adj sheet and electronic adj circuit adj chip)	12/5/2007	A.G.
·		
•		
		·